



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Guenter Stenz et al.
Assignee: Xilinx, Inc.
Title: Method System and Apparatus for Floorplanning Programmable Logic Designs
Serial No.: 10/787,326 File Date: 02-26-04
Examiner: Vuthe Siek Art Unit: 2825
Docket No.: X-1478 US Conf. No.: 3972

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed references are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

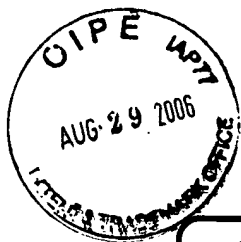
08/30/2006 ZJUHR1 00000025 240040 10787326
01 FC:1806 180.00 DA

Keith A. Chanroo
Attorney for Applicants
Reg. No. 36,480

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on August 23, 2006.

Pat Tompkins
Name

Signature



PTO/SB/08A (10-01)
Approved for use through 10/31/2002. OMB 0851-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application / Conf. No.	10/787,326 / 3972		
		Filing Date	February 26, 2004		
		First Named Inventor	Gunter Stenz		
		Art Unit	2825		
		Examiner Name	Vuthe Siek		
Sheet	1	of	1	Attorney Docket Number	X-1478 US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Hiroshi Murata et al.; "VLSI Module Placement Based on Rectangle-Packing by the Sequence-Pair"; IEEE Transactions on computer-Aided Design of Integrated Circuits and Systems; Vol. 15, No. 2; December 1996; pp. 1518-1524.	
		S. Kirkpatrick et al.; "Optimization by Simulated Annealing"; Science; Volume 220, Number 4598; 13 May 1983; pp. 671-680.	
		Dieter Jungnickel; Graphs, Networks and Algorithms; "Algorithms and Computation in Mathematics"; Chapter 13 - Weighted Matchings; Volume 5; Copyright 1999; published by Springer-Verlag Berlin Heidelberg New York; pp. 389-401.	

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.